

## AMENDMENTS TO THE SPECIFICATION

✓  
Please replace the paragraph beginning at page 67, line 14 with the following amended paragraph:

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a1  
Pin 84 of the 68331  $\mu$ C is coupled directly to +5VD and is also coupled to DGND through a parallel combination of a 1  $\mu$ F capacitor and a 0.01  $\mu$ F capacitor as shown in Figs. 62D, 62E, and 62G. In addition, pin 84 of the 68331  $\mu$ C is coupled to pin 87 of the 68331  $\mu$ C through a 0.1 microfarad capacitor as shown in Figs. 62D, 62E, 62G and 62H. Pin 83 of the 68331  $\mu$ C is coupled to one terminal of a 10 M $\Omega$  resistor and pin 85 of the 68331  $\mu$ C is coupled to the other terminal of the 10 M $\Omega$  resistor as shown in Figs. 62E and 62H. Pin 83 of the 68331  $\mu$ C is also coupled to one terminal of a ~~32768 kHz~~ 32.768 kHz oscillator or clock through a 332 k $\Omega$  resistor and pin 85 is coupled to the other terminal of the ~~32768 kHz~~ 32.768 kHz clock. The two terminals of the ~~32768 kHz~~ 32.768 kHz clock are each coupled to DGND through respective ~~45 pF~~ 15 pF capacitors.

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